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{Dual-Band Transmitters Using Digitally Predistorted Frequency Multipliers for Reconfigurable Design of FET Frequency Multipliers and Harmonic Oscillators

### **Ieee electron device letters, vol. 30, no. 5, may**

NO. 5, MAY 2009 547 Graphene Frequency Multipliers Han Wang, Daniel Nezich, Jing Kong, E. Camargo, Design of FET Frequency Multipliers and Harmonic Oscillators.

### **A high order frequency multiplier for low power**

TECHNICAL FEATURE A High Order Frequency Multiplier for Low Power AZ Direct frequency multipliers are commonly used in Design of FET Multipliers and

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The world of electronic design solutions, Oscillators & Crystals Transistor Oscillator Pierce Harmonic Oscillator FET Frequency Multipliers and Harmonic

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A tutorial paper on active frequency multiplier design is for visualizing and understanding concepts important to frequency multiplier design,

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Design of GaAs FET: Frequency Multipliers and Harmonic Oscillators. The story on this book starts in the summer of 1995. At that time I was working for Hewlett

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Design of 60 Millimeter-Wave Frequency Doubler using Distributed Structure Design of FET Frequency Multipliers and Harmonic Oscillators,

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The nonlinear FET modeling work High-efficiency bipolar microwave frequency multipliers with A simple design method for harmonic oscillators

### **A 8/16 ghz frequency doubler mmic for space**

A 8/16 GHz Frequency Doubler MMIC for Space Application 1Edmar Camargo, Design of FET Frequency Multipliers and Harmonic Oscillators, Norwood: Artech House, 1998.

### **Frequency multiplier from 6 to 12 ghz using a**

Frequency multiplier A design method for frequency multipliers With this technique a frequency multiplier from 6 to 12 GHz was designed using the dual gate

### **Large-signal approach yields low-noise vhf/uhf**

"Large Signal GaAs MESFET Oscillator Design," IEEE Transactions on Microwave Edmar Camargo, Design of FET Frequency Multipliers and Harmonic Oscillators,

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In the balanced frequency doubler design, E. Camargo, Design of FET Frequency Multipliers and Harmonic Oscillators, Artech House, Norwood, Mass, USA, 1998.

### **Design of a GaAs fet frequency tripler (2.5-7.5**

Design of a GaAs FET frequency tripler (2.5-7.5 GHz) for the payloads of communication satellites: Frequency Multipliers, Gallium Arsenides, Harmonics,

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FET ACTIVE FREQUENCY MULTIPLIER DESIGN EXPERTISE. From UHF to Ku band; X2, X3, X4, X5 multiplication; Hybrid or discrete assembly; Small package, or pallet (carrier

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as a frequency multiplier. Unlike the conventional balanced FET-based multiplier, Advanced design system simulations are used to compare the performance

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