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USING ACTIVE COMPONENTS TO PERFORM concluding this paper, VLSI Circuits, 1988. Digest of Technical Papers.,

### **1988 symposium on vlsi circuits, tokyo (japan),**

1988 Symposium on VLSI circuits, Tokyo (Japan), Title: A large VLSI fully interconnected neural network - VLSI Circuits, 1988. Digest of Technical Papers.,

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10-100 gb/s throughput cmos techniques - VLSI Circuits, 1999. Digest of Technical Papers. 1999 Symposium on Author: IEEE Created Date: 10/28/1999 11:46:55 AM

### **A 2ghz merged cmos lna and mixer for wcdma**

conference paper. published yes Ali Karimi-Sanjaani author Professor Henrik Sj land author tde-hsj Professor Asad Abidi author Department of Electroscience

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IBM Research VLSI Design D. Heidel, M. Immediato, K. Jenkins, ISSCC Digest of Technical Papers Proc. of Tech. Papers, 1999 International Symposium on VLSI

### **Shouri chatterjee: publications**

Publications. N. Nallam, S on VLSI Circuits, Digest of Technical Papers, pp. 56 for 0.5 V analog integrated filters", Symposium on VLSI Circuits, Digest of

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2000 Symposium on Vlsi Circuits: Digest of Technical Papers Symposium on VLSI Circuits. 2000 Symposium on Vlsi Circuits: Digest of Technical Papers June 15

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A Low-Power Video Motion Estimation Array Processor - VLSI Circuits, 1996. Digest of Technical Papers., 1996 Symposium on Author: IEEE Created Date:

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Programmable termination for CML I/O's in high speed CMOS transceivers - VLSI Circuits Digest of Technical Papers, 2002. Symposium on Created Date:

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(1990): 259-299. Symposium on VLSI Circuits, Digest of Technical Papers (2004): IEEE International Symposium on Circuits and Systems 5

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Perimeter and plug effects in deep sub-micron polysilicon emitter bipolar transistors - VLSI Technology, 1990. Digest of Technical Papers., 1990 Symposium on

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**Biologically inspired visual motion detection in**

Biologically Inspired Visual Motion Detection in VLSI In Proc. 1990 IEEE Symposium on Systems, In Digest of Technical Papers of ISSCC'2001,

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1983 Symposium on VLSI Technology. Digest of Technical Papers 1983 Symposium on VLSI Technology. Digest of Transmission Line Model for Latchup in CMOS Circuits

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